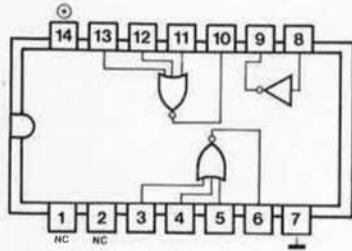


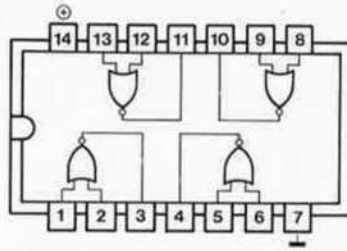
DUAL 3-INPUT NOR-GATE PLUS INVERTER

4000



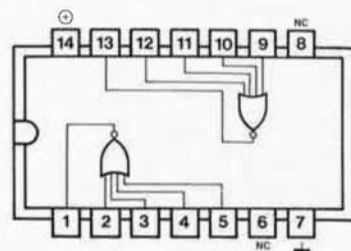
QUADRUPLE 2-INPUT NOR-GATE

4001



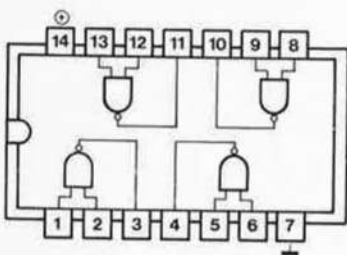
DUAL 4-INPUT NOR-GATE

4002



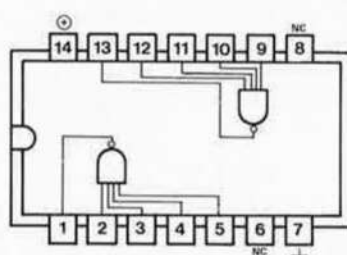
QUADRUPLE 2-INPUT NAND-GATE

4011



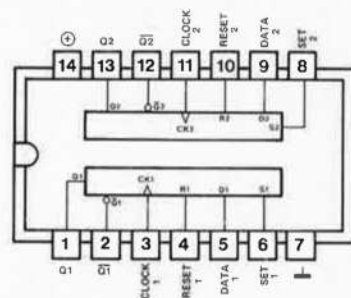
DUAL 4-INPUT NAND-GATE

4012



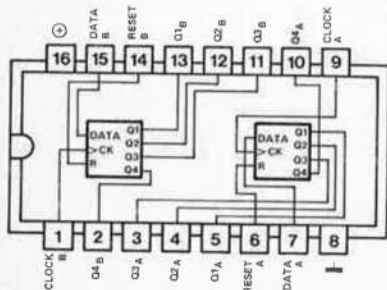
DUAL D-FLIP-FLOP

4013



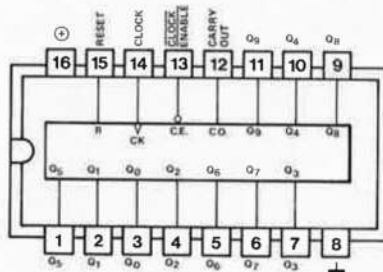
DUAL 4-BIT STATIC SHIFT REGISTER

4015



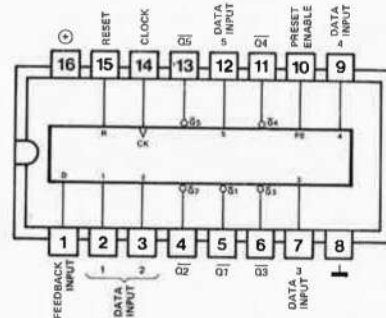
DECADE COUNTER/DIVIDER

4017



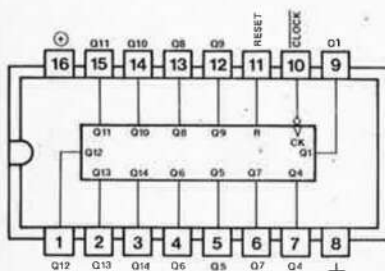
SYNCHRONOUS PRESETTABLE DIVIDE BY 'N' COUNTER

4018



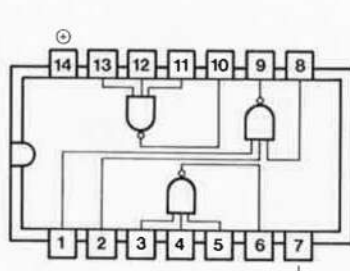
14-BIT BINARY RIPPLE COUNTER

4020



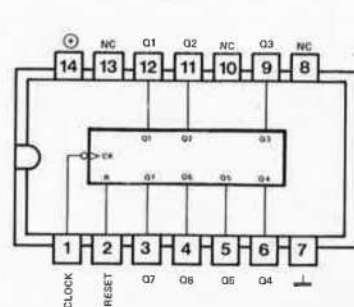
TRIPLE 3-INPUT NAND-GATE

4023



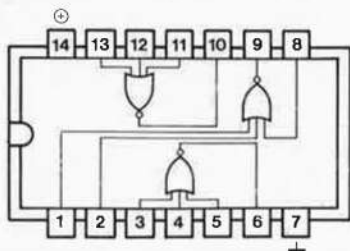
7-STAGE BINARY RIPPLE COUNTER

4024



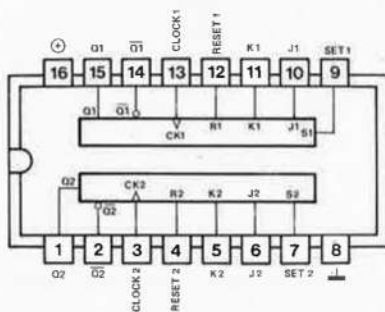
TRIPLE 3 INPUT NOR GATE

4025



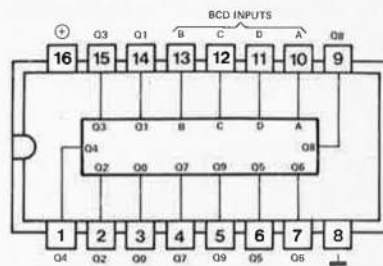
DUAL JK-FLIP-FLOP

4027

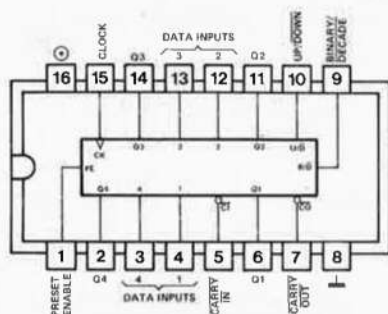


BCD-TO-DECIMAL DECODER

4028

SYNCHRONOUS PRESET TABLE BINARY/DECADE
UP/DOWN COUNTER

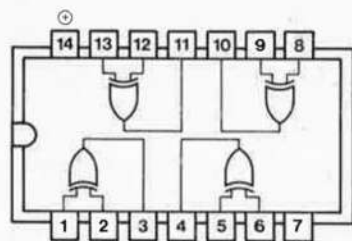
4029



QUADRUPLE 2-INPUT EXCLUSIVE OR GATE

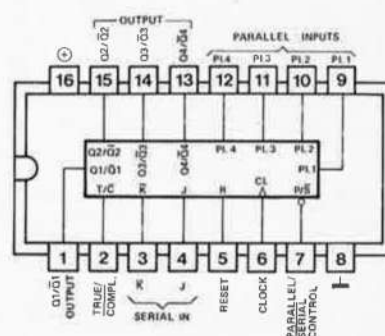
4030

4070 low power TTL compatible (fan out = 2)



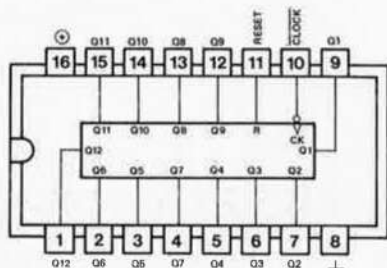
4 BIT PARALLEL-IN/PARALLEL-OUT SHIFT REGISTER

4035



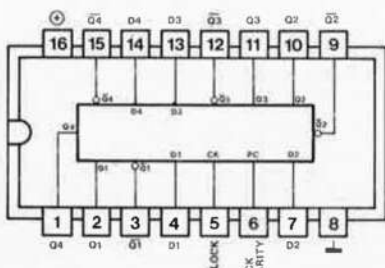
12 BIT BINARY RIPPLE COUNTER

4040



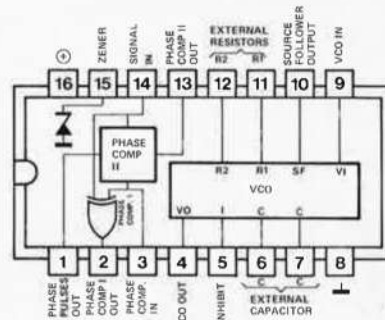
QUAD Clocked "D" LATCH

4042



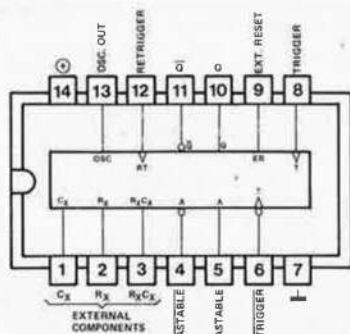
MICROPOWER PLL

4046



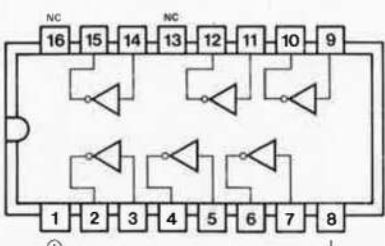
MONOSTABLE/ASTABLE MULTIVIBRATOR

4047



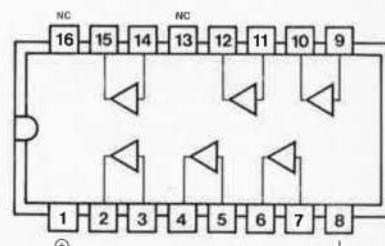
HEX INVERTING BUFFER

4049



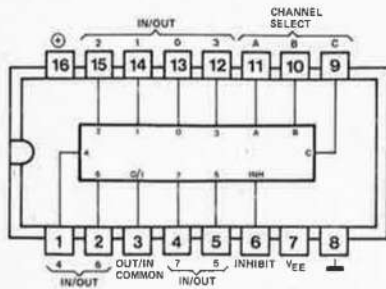
HEX BUFFER

4050



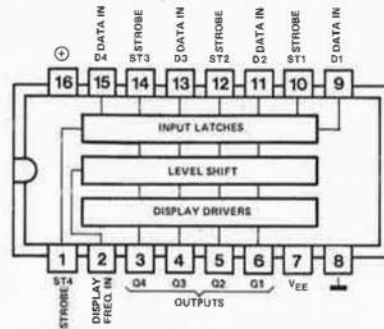
8 CHANNEL ANALOG MULTIPLEXER/DEMULIPLEXER

4051



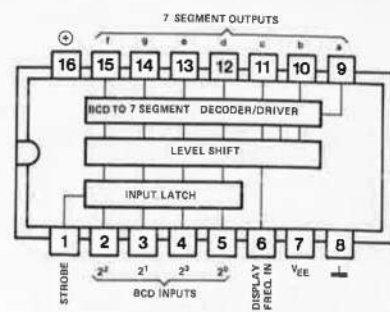
4 SEGMENT LCD DRIVER

4054



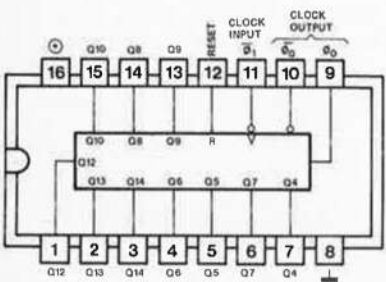
BCD TO 7 SEGMENT DECODER/LCD DRIVER

4056



14 STAGE BINARY RIPPLE COUNTER AND OSCILLATOR

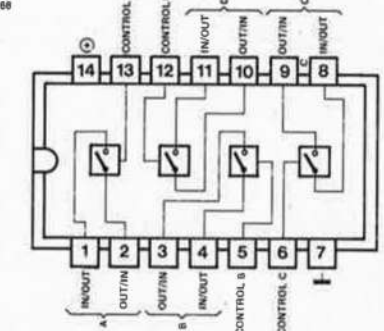
4090



QUAD BILATERAL SWITCH

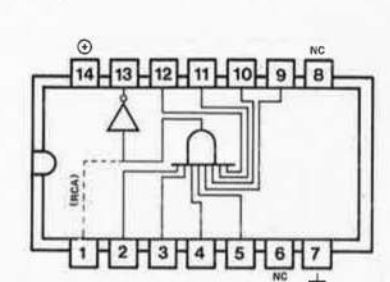
4016

4068



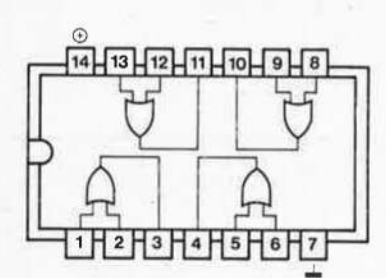
8 INPUT AND/NAND GATE

4068



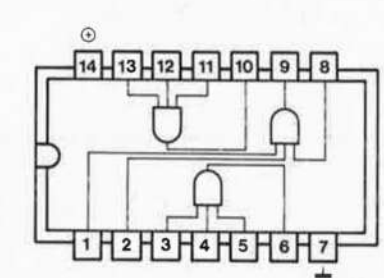
QUADRUPLE 2-INPUT OR GATE

4071



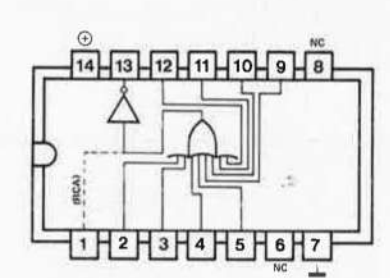
TRIPLE 3-INPUT AND GATE

4073



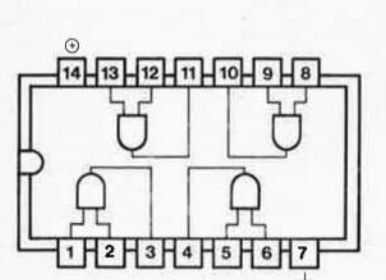
8 INPUT OR/NOR GATE

4078



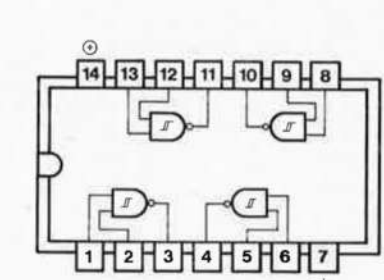
QUADRUPLE 2-INPUT AND GATE

4081



QUADRUPLE 2-INPUT NAND SCHMITT-TRIGGER

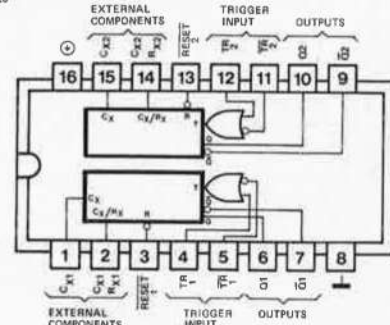
4083



DUAL MONOSTABLE MULTIVIBRATOR

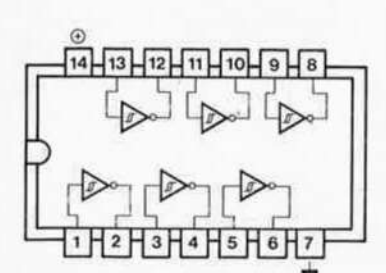
4090

4529



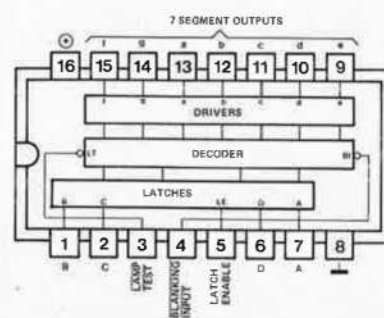
HEX SCHMITT-TRIGGER

40106



BCD TO 7 SEGMENT LATCH/DECODER/DRIVER

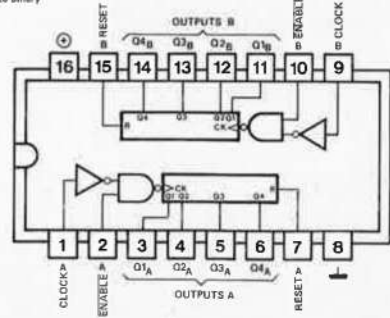
4511



DUAL 4-BIT SYNCHRONOUS UP-COUNTERS

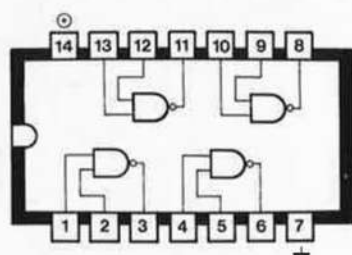
4518 BCD

4520 binary



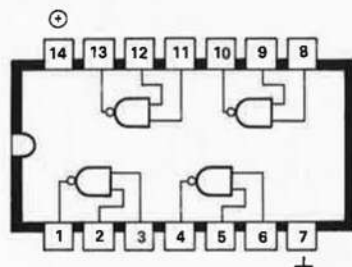
QUADRUPLE 2-INPUT NAND GATE

7400
7403 open collector outputs
7437 power driver (fan out = 30)



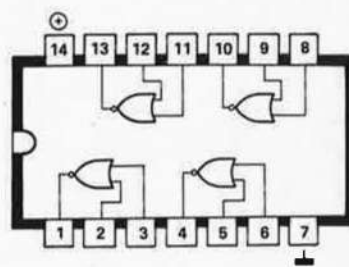
QUADRUPLE 2-INPUT NAND GATE WITH OPEN COLLECTOR OUTPUT

7401



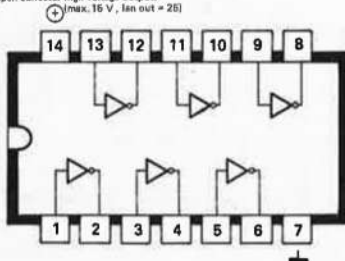
QUADRUPLE 2-INPUT NOR GATE

7402
7428 power driver (fan out = 30)



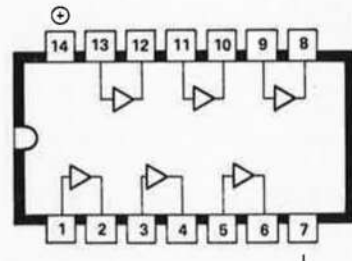
HEX INVERTER

7404
7405 open collector outputs
7406 open collector high voltage outputs (max. 30 V, fan out = 25)
7418 open collector high voltage outputs (max. 16 V, fan out = 25)



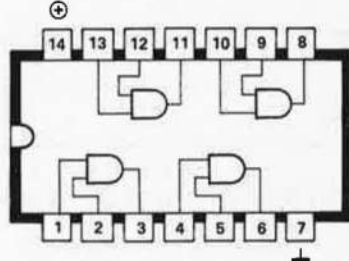
HEX BUFFER/DRIVER WITH OPEN-COLLECTOR HIGH VOLTAGE OUTPUTS (max. 30 V, fan out = 25)

7407



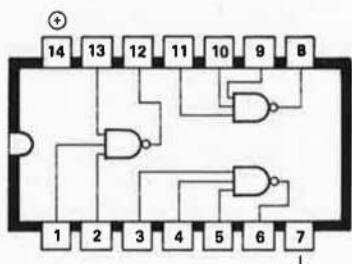
QUADRUPLE 2-INPUT AND GATE

7408
7409 open collector outputs



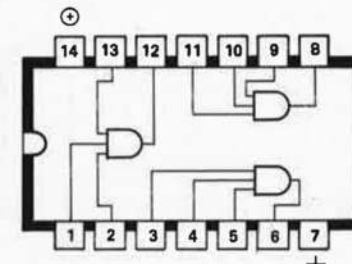
TRIPLE 3-INPUT NAND GATE

7410
7412 open collector outputs



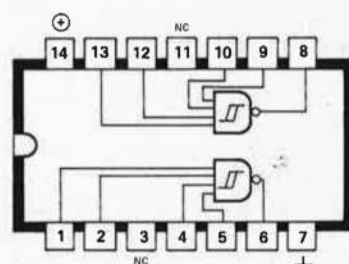
TRIPLE 3-INPUT AND GATE

7411



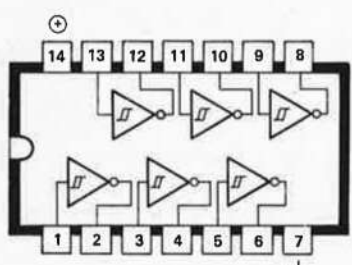
DUAL 4-INPUT NAND SCHMITT TRIGGER

7413



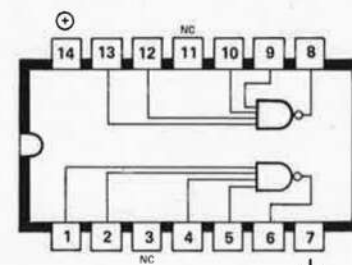
HEX SCHMITT TRIGGER INVERTER

7414



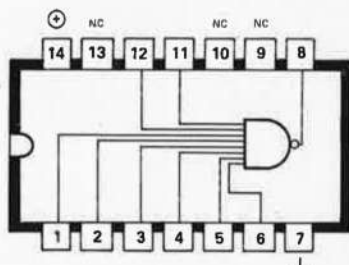
DUAL 4-INPUT NAND GATE

7420
7440 power driver (fan out = 30)



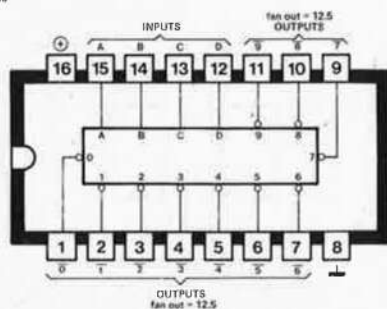
6-INPUT NAND GATE

7430



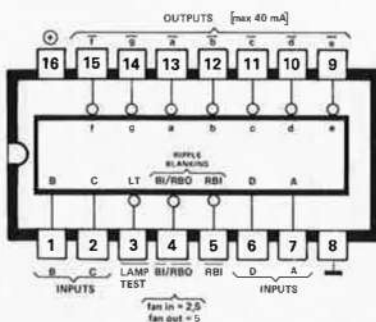
BCD TO DECIMAL DECODER/DRIVER WITH OPEN COLLECTOR OUTPUTS (max. 30 V)

7445



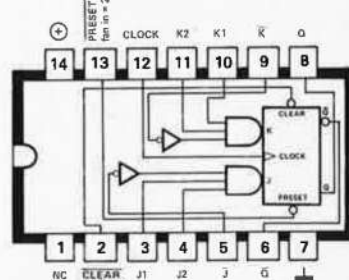
BCD TO 7 SEGMENT DECODER/DRIVER WITH OPEN COLLECTOR OUTPUTS

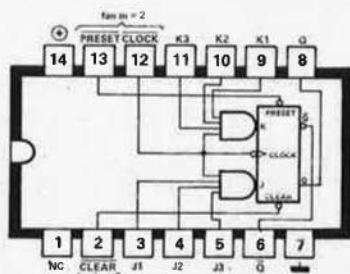
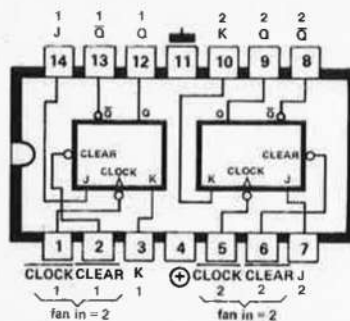
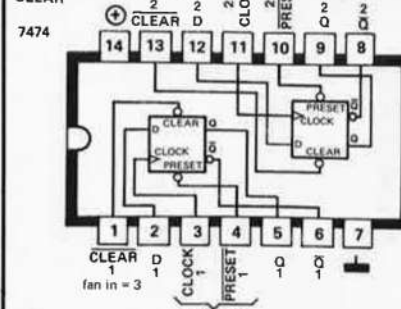
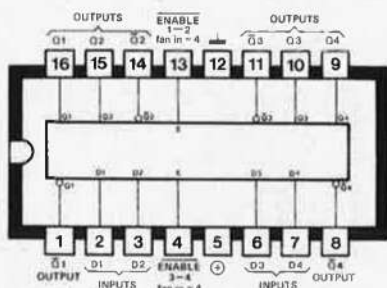
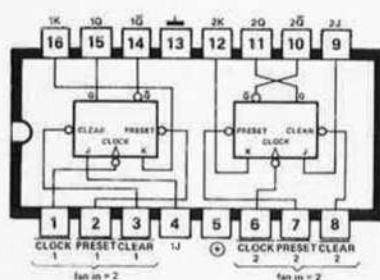
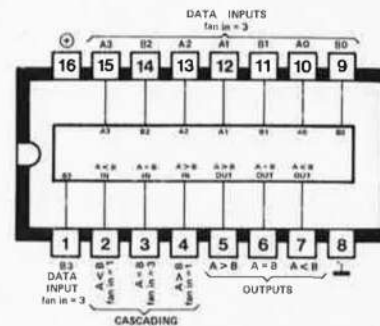
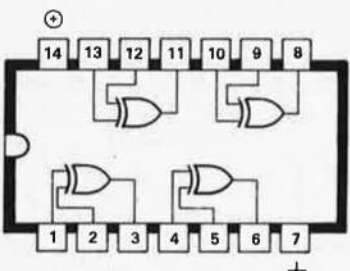
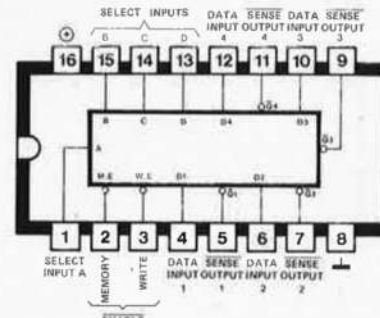
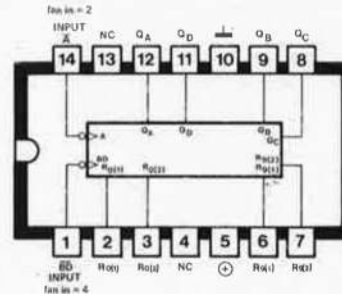
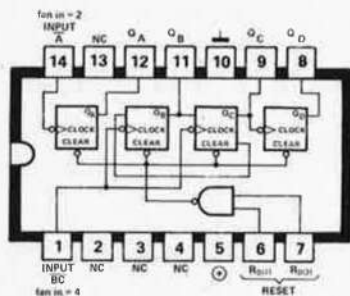
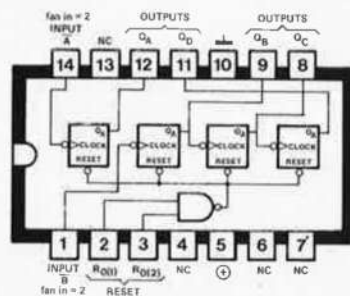
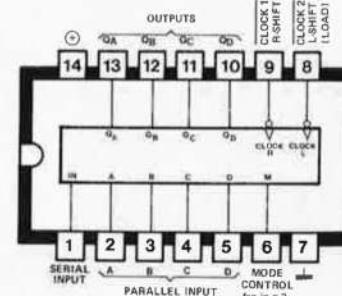
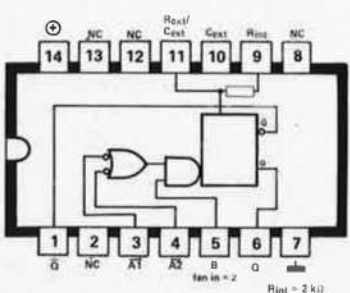
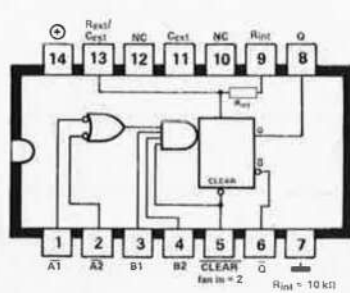
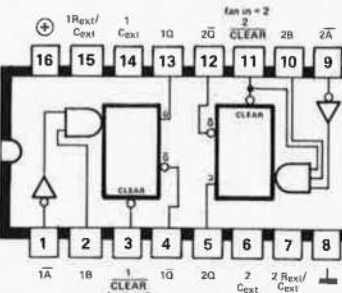
7447



AND GATED J-K POSITIVE EDGE-TRIGGERED FLIP-FLOP WITH PRESET AND CLEAR

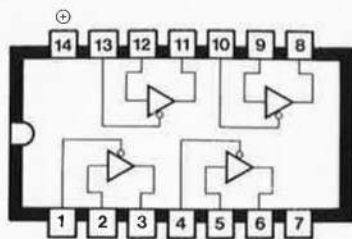
7470



AND GATED J-K MASTER-SLAVE FLIP-FLOP
WITH PRESET AND CLEAR
7472DUAL J-K FLIP-FLOP WITH CLEAR
7473DUAL D-TYPE POSITIVE-EDGE-
TRIGGERED FLIP-F WITH
PRESET AND
CLEAR
74744-BIT BISTABLE LATCH
7475DUAL J-K MASTER-SLAVE FLIP-FLOP
WITH PRESET AND CLEAR
74764-BIT COMPARATOR
7485QUADRUPLE 2-INPUT EXCLUSIVE OR GATE
748664-BIT READ/WRITE MEMORY WITH OPEN COLLECTOR OUTPUTS
(fan out = 7.5)
7489DECADE COUNTER
7490DIVIDE-BY-TWELVE COUNTER (÷ 2 and ÷ 6)
74924-BIT BINARY COUNTER
74934-BIT PARALLEL-IN PARALLEL-OUT SHIFT REGISTER
7495MONOSTABLE MULTIVIBRATOR
74121RETRIGGERABLE MONOSTABLE MULTIVIBRATOR WITH CLEAR
74122DUAL RETRIGGERABLE MONOSTABLE MULTIVIBRATOR WITH CLEAR
74123

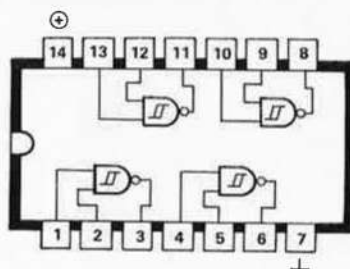
QUAD BUFFER (3-STATE)

74126

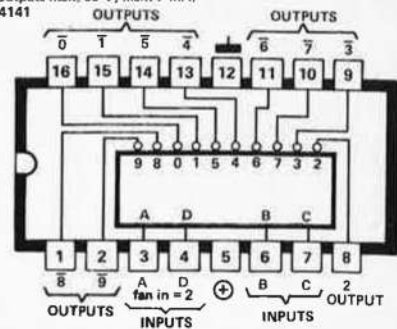


QUADRUPLE 2-INPUT NAND SCHMITT TRIGGER

74132

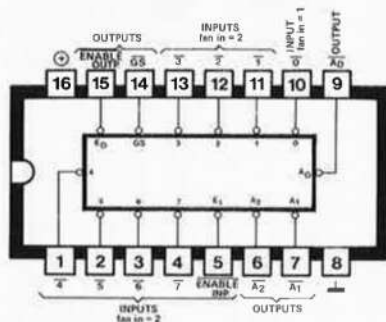
BCD-TO-DECIMAL DECODER/DRIVER
WITH OPEN COLLECTOR OUTPUTS
(outputs max. 60 V, max. 7 mA)

74141



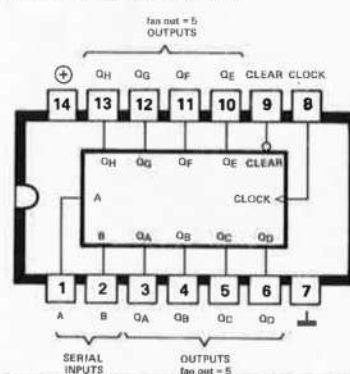
PRIORITY ENCODER

74148



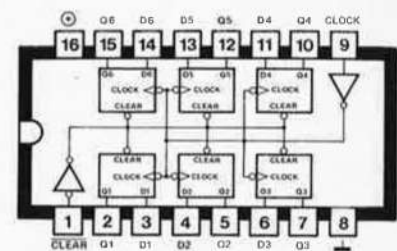
8-BIT SERIAL-IN PARALLEL-OUT SHIFT REGISTER

74164



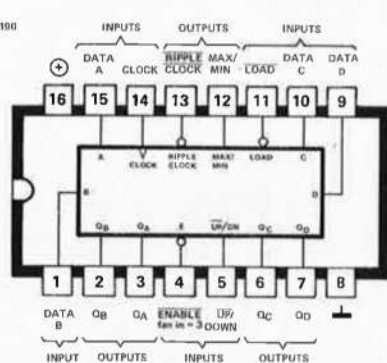
HEX D-FLIP-FLOP WITH CLEAR

74174



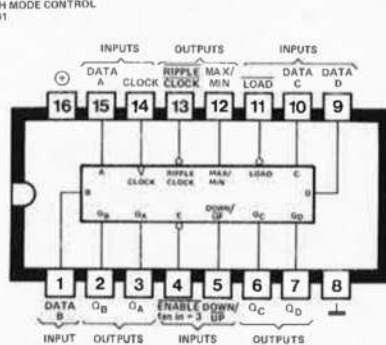
SYNCHRONOUS BCD UP/DOWN COUNTER WITH MODE CONTROL

74190



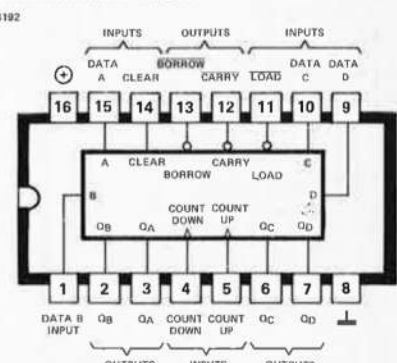
SYNCHRONOUS 4-BIT BINARY UP/DOWN COUNTER WITH MODE CONTROL

74191



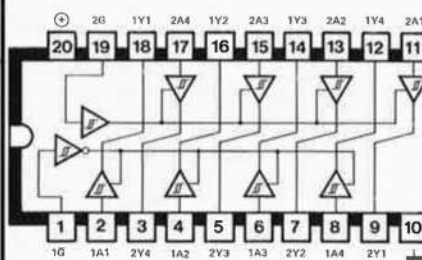
SYNCHRONOUS UP/DOWN DECADE COUNTER

74192



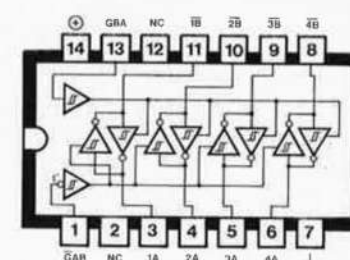
OCTAL BUFFER AND LINE DRIVER (3-STATE)

74LS241



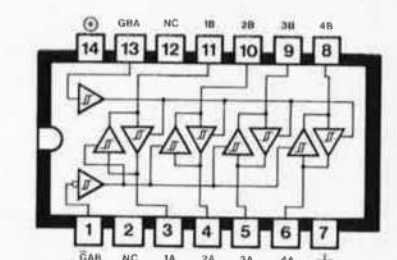
QUADRUPLE BUS TRANSCEIVER (3-STATE)

74LS242



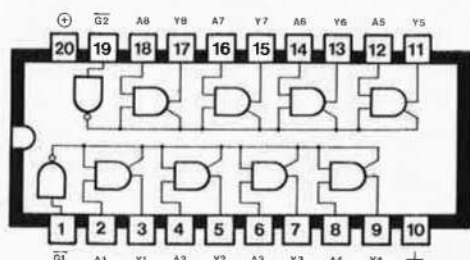
QUADRUPLE BUS TRANSCEIVER (3-STATE)

74LS243



OCTAL BUFFER (3-STATE)

81LS97



OCTAL BUFFER (3-STATE)

81LS96

