

PRODUCTS

DESIGN

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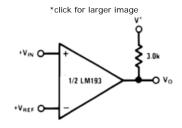
## LM2903 - Low Power Low Offset Voltage Dual Comparator

Features

# Wide supply Voltage range: 2.0V to 36V Single or dual supplies: ±1.0V to ±18V

Very low supply current drain (0.4 mA) - independent of supply voltage	
Low input biasing current:	25 nA
Low input offset current:	±5 nA
Maximum offset voltage:	±3 mV
Input common-mode voltage range includes ground	
Differential input voltage range equal to the power supply voltage	
Low output saturation voltage,:	250 mV at 4 mA
Output voltage compatible with TTL, DTL, ECL, MOS and CMOS logic systems	
Available in the 8-Bump (12 mil) micro SMD package	
See AN-1112 for micro SMD considerations	

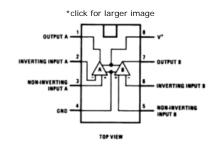
#### **Typical Application**



#### Parametric Table

Response Time	0.4 us
Output Bus	Open Drain
Supply Min	2 Volt
Supply Max	36 Volt
Channels	2 Channels
Offset Voltage max, 25C	7 mV
Output Current	16 mA
Input Range	Vcm to V-
Supply Current Per Channel	0.2 mA
PowerWise Rating 3	80 uA x us
Max Input Bias Current	500 nA
Special Features	Undefined
Temperature Min	-40 deg C
Temperature Max	85 deg C
Function	Comparator

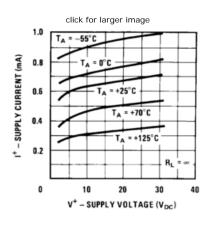
#### **Connection Diagram**



### Typical Performance

#### **Applications**

- High precision comparators
- ullet Reduced  $V_{os}$  drift over temperature
- Eliminates need for dual supplies
- Allows sensing near ground
- Compatible with all forms of logic
- Power drain suitable for battery operation





LM193/LM293/LM393/LM2903 Low Power Low Offset Voltage Dual Comparators

LM193/LM293/LM393/LM2903 Low Power Low Offset Voltage Dual Comparators (Japanese)

#### Package Availability, Models

			P	ackage				Factory Le	ad Time				Std	Package			
Part Number	Туре	Pins	Spec.	MSL Rating	Peak Reflow	RoHS Report		Weeks	Qty	Models			Pack Size	Marking Format			
1.0000004	COIC NADDOW		STD	1	235	D-UC		Full prod	uction	NI/A			rail	NSZXTT LM			
LM2903M	SOIC NARROW	8	NOPB	1	260	KOHS	S 6 weeks 5000 N/A			RoHS 6 we						of 95	2903M
LM2903MX	SOIC NARROW	8	STD	1	235	RoHS Full production		Full production		Full production		N/A			reel of	NSZXTT LM	
LIVIZ903IVIA	SOIC NARROW	0	NOPB	1	260		10000				2500	2903M					
LAMOOODL		A LIDIO	MDIP		STD	1	NA	D.110		Full prod	uction	N1/A			rail	NSUZXYTT	
LM2903N	MDIP	8	NOPB	1	NA	RoHS		6 weeks	2000	N/A	I/A		of 40	LM 2903N			
I 1 400 00 I T	141000 0140		STD	1	260	- III		Full production		Full production					reel	XVC	
LM2903ITL	MICRO SMD	8	NOPB	1	260	RoHS		6 weeks 2000			N/A				of 250	103	
	12903ITLX MICRO SMD		STD	1	260			Full prod	uction				reel	XVC			
LM2903ITLX		MICRO SMD	8	NOPB	1	260	RoHS		6 weeks	5000	N/A			of 3000	103		

#### **General Description**

The LM193 series consists of two independent precision voltage comparators with an offset voltage specification as low as 2.0 mV max for two comparators which were designed specifically to operate from a single power supply over a wide range of voltages. Operation from split power supplies is also possible and the low power supply current drain is independent of the magnitude of the power supply voltage. These comparators also have a unique characteristic in that the input common-mode voltage range includes ground, even though operated from a single power supply voltage.

Application areas include limit comparators, simple analog to digital converters; pulse, squarewave and time delay generators; wide range VCO; MOS clock timers; multivibrators and high voltage digital logic gates. The LM193 series was designed to directly interface with TTL and CMOS. When operated from both plus and minus power supplies, the LM193 series will directly interface with MOS logic where their low power drain is a distinct advantage over standard comparators.

The LM393 and LM2903 parts are available in National's innovative thin micro SMD package with 8 (12 mil) large bumps.

#### **Reliability Metrics**

Part Number	Process	EFR Reject	EFR Sample Size	PPM *	LTA Rejects	LTA Device Hours	FITS	MTTF (Hours)
LM2903ITL	SLM	0	42786	0	0	3352500	2	951281028
LM2903ITLX	SLM	0	42786	0	0	3352500	2	951281028
LM2903M	SLM	0	42786	0	0	3352500	2	951281028
LM2903MX	SLM	0	42786	0	0	3352500	2	951281028
LM2903N	SLM	0	42786	0	0	3352500	2	951281028

Note: The Early Failure Rates were calculated as point estimates. The Long Term Failure Rates were calculated at 60% confidence using the Arrhenius equation at 0.7eV activation energy and derating the assumed stress temperature of 150°C to an application temperature of 55°C.



## LM193/LM293/LM393/LM2903 Low Power Low Offset Voltage Dual Comparators

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## **Advantages**

- High precision comparators
- Reduced V<sub>OS</sub> drift over temperature
- Eliminates need for dual supplies
- Allows sensing near ground
- Compatible with all forms of logic
- Power drain suitable for battery operation

#### **Features**

- Wide supply
  - Voltage range:Single or dual supplies:2.0V to 36V±1.0V to ±18V
- Very low supply current drain (0.4 mA) independent of supply voltage

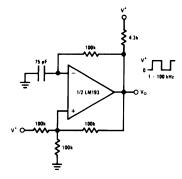
■ Low input biasing current: 25 nA

■ Low input offset current: ±5 nA

■ Maximum offset voltage: ±3 mV

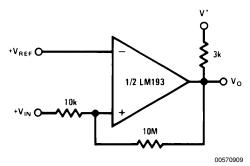
- Input common-mode voltage range includes ground
- Differential input voltage range equal to the power supply voltage
- Low output saturation voltage,: 250 mV at 4 mA
- Output voltage compatible with TTL, DTL, ECL, MOS and CMOS logic systems
- Available in the 8-Bump (12 mil) micro SMD package
- See AN-1112 for micro SMD considerations

#### **Squarewave Oscillator**

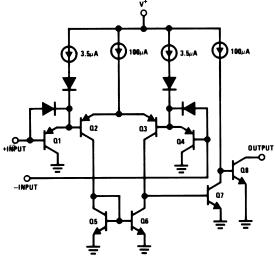


00570938

#### Non-Inverting Comparator with Hysteresis

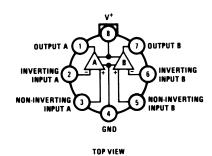


## **Schematic and Connection Diagrams**



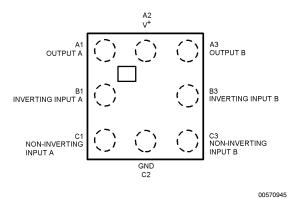
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Metal Can Package



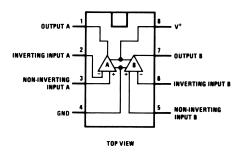
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micro SMD



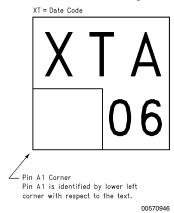
**Top View** 

## **Dual-In-Line/SOIC Package**



00570901

### micro SMD Marking



**Top View** 

## **Absolute Maximum Ratings** (Note 10)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Supply Voltage, V $^+$  36V Differential Input Voltage (Note 8) 36V Input Voltage -0.3V to +36V Input Current (V $_{\rm IN}$ <-0.3V) (Note 3) 50 mA Power Dissipation (Note 1)

Molded DIP780 mWMetal Can660 mWSmall Outline Package510 mWmicro SMD Pacakge568mW

Output Short-Circuit to Ground

(Note 2) Continuous

Operating Temperature Range

LM393 0°C to +70°C LM293 -25°C to +85°C Storage Temperature Range

Lead Temperature

(Soldering, 10 seconds) +260°C

Soldering Information

Dual-In-Line Package
Soldering (10 seconds) 260°C

Small Outline Package 215°C

Vapor Phase (60 seconds)

Infrared (15 seconds) 220°C

See AN-450 "Surface Mounting Methods and Their Effect on Product Reliability" for other methods of soldering surface mount devices.

ESD rating

(1.5 k $\Omega$  in series with 100 pF) 1300V

## **Electrical Characteristics**

 $(V^+=5V, T_A = 25^{\circ}C, unless otherwise stated)$ 

Parameter	Conditions			LM193A			
			Min	Тур	Max		
Input Offset Voltage	(Note 9)	(Note 9)			2.0	mV	
Input Bias Current	I <sub>IN</sub> (+) or I <sub>IN</sub>	(-) with Output In Linear		25	100	nA	
	Range, V <sub>C</sub>	<sub>M</sub> = 0V (Note 5)					
Input Offset Current	I <sub>IN</sub> (+)-I <sub>IN</sub> (-	) V <sub>CM</sub> = 0V		3.0	25	nA	
Input Common Mode	V+ = 30V	(Note 6)	0		V+-1.5	V	
Voltage Range							
Supply Current	R <sub>L</sub> =∞	V+=5V		0.4	1	mA	
		V+=36V		1	2.5	mA	
Voltage Gain	R <sub>L</sub> ≥15 kΩ,	V <sup>+</sup> =15V	50	200		V/mV	
	$V_O = 1V \text{ to}$	11V					
Large Signal Response	V <sub>IN</sub> =TTL L	ogic Swing, V <sub>REF</sub> =1.4V		300		ns	
Time	V <sub>RL</sub> =5V, R	<sub>L</sub> =5.1 kΩ					
Response Time	V <sub>RL</sub> =5V, R	L=5.1 kΩ (Note 7)		1.3		μs	
Output Sink Current	V <sub>IN</sub> (-)=1V,	$V_{IN}(-)=1V, V_{IN}(+)=0, V_{O}\approx 1.5V$		16		mA	
Saturation Voltage	V <sub>IN</sub> (-)=1V,	V <sub>IN</sub> (-)=1V, V <sub>IN</sub> (+)=0, I <sub>SINK</sub> ≤4 mA		250	400	mV	
Output Leakage Current	V <sub>IN</sub> (-)=0, \	$V_{IN}(-)=0, V_{IN}(+)=1V, V_{O}=5V$				nA	

### **Electrical Characteristics**

(V<sup>+</sup>=5V,  $T_A = 25$ °C, unless otherwise stated)

Parameter	Conditions	LM193		LM293, LM393				Units			
		Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	
Input Offset Voltage	(Note 9)		1.0	5.0		1.0	5.0		2.0	7.0	mV
Input Bias Current	$I_{IN}(+)$ or $I_{IN}(-)$ with Output In		25	100		25	250		25	250	nA
	Linear Range, V <sub>CM</sub> = 0V (Note 5)										
Input Offset Current	$I_{IN}(+)-I_{IN}(-) V_{CM} = 0V$		3.0	25		5.0	50		5.0	50	nA
Input Common Mode	V+ = 30V (Note 6)	0		V+-1.5	0		V+-1.5	0		V+-1.5	V
Voltage Range											

## **Electrical Characteristics** (Continued)

(V<sup>+</sup>=5V,  $T_A = 25^{\circ}C$ , unless otherwise stated)

Parameter	Conditions			LM193			LM293, LM393			LM2903		
			Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	]
Supply Current	R <sub>L</sub> =∞	V+=5V		0.4	1		0.4	1		0.4	1.0	mA
		V+=36V		1	2.5		1	2.5		1	2.5	mA
Voltage Gain	R <sub>L</sub> ≥15 kΩ, \	V <sup>+</sup> =15V	50	200		50	200		25	100		V/mV
	$V_O = 1V$ to	11V										
Large Signal Response	V <sub>IN</sub> =TTL Lo	gic Swing, V <sub>REF</sub> =1.4V		300			300			300		ns
Time	$V_{RL}=5V, R_{L}$	=5.1 kΩ										
Response Time	V <sub>RL</sub> =5V, R <sub>L</sub>	=5.1 kΩ (Note 7)		1.3			1.3			1.5		μs
Output Sink Current	$V_{IN}(-)=1V,$	V <sub>IN</sub> (+)=0, V <sub>O</sub> ≤1.5V	6.0	16		6.0	16		6.0	16		mA
Saturation Voltage	$V_{IN}(-)=1V,$	V <sub>IN</sub> (+)=0, I <sub>SINK</sub> ≤4 mA		250	400		250	400		250	400	mV
Output Leakage Current	$V_{IN}(-)=0, V$	<sub>IN</sub> (+)=1V, V <sub>O</sub> =5V		0.1			0.1			0.1		nA

## **Electrical Characteristics**

(V+ = 5V) (Note 4)

Parameter	Conditions		LM193A				
		Min	Тур	Max			
Input Offset Voltage	(Note 9)			4.0	mV		
Input Offset Current	$I_{IN(+)} - I_{IN(-)}, V_{CM} = 0V$			100	nA		
Input Bias Current	I <sub>IN</sub> (+) or I <sub>IN</sub> (-) with Output in Linear Range,			300	nA		
	V <sub>CM</sub> =0V (Note 5)						
Input Common Mode	V+=30V (Note 6)	0		V <sup>+</sup> -2.0	V		
Voltage Range							
Saturation Voltage	$V_{IN}(-)=1V, V_{IN}(+)=0, I_{SINK} \le 4 \text{ mA}$			700	mV		
Output Leakage Current	$V_{IN}(-)=0, V_{IN(+)}=1V, V_{O}=30V$			1.0	μA		
Differential Input Voltage	Keep All V <sub>IN</sub> 's≥0V (or V <sup>-</sup> , if Used), (Note 8)			36	V		

## **Electrical Characteristics**

(V+ = 5V) (Note 4)

Parameter	Conditions		LM193		LM293, LM393				Units		
		Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	
Input Offset Voltage	(Note 9)			9			9		9	15	mV
Input Offset Current	$I_{IN(+)}-I_{IN(-)}, V_{CM}=0V$			100			150		50	200	nA
Input Bias Current	I <sub>IN</sub> (+) or I <sub>IN</sub> (-) with Output in Linear Range, V <sub>CM</sub> =0V (Note 5)			300			400		200	500	nA
Input Common Mode Voltage Range	V+=30V (Note 6)	0		V <sup>+</sup> –2.0	0		V+-2.0	0		V <sup>+</sup> -2.0	V
Saturation Voltage	$V_{IN}(-)=1V, V_{IN}(+)=0,$ $I_{SINK} \le 4 \text{ mA}$			700			700		400	700	mV
Output Leakage Current	$V_{IN}(-)=0, V_{IN(+)}=1V, V_{O}=30V$			1.0			1.0			1.0	μA
Differential Input Voltage	Keep All V <sub>IN</sub> 's≥0V (or V <sup>-</sup> , if Used), (Note 8)			36			36			36	V

Note 1: For operating at high temperatures, the LM393 and LM2903 must be derated based on a 125°C maximum junction temperature and a thermal resistance of 170°C/W which applies for the device soldered in a printed circuit board, operating in a still air ambient. The LM193/LM193A/LM293 must be derated based on a 150°C maximum junction temperature. The low bias dissipation and the "ON-OFF" characteristic of the outputs keeps the chip dissipation very small ( $P_D \le 100 \text{ mW}$ ), provided the output transistors are allowed to saturate.

**Note 2:** Short circuits from the output to V<sup>+</sup> can cause excessive heating and eventual destruction. When considering short circuits to ground, the maximum output current is approximately 20 mA independent of the magnitude of V<sup>+</sup>.

Note 3: This input current will only exist when the voltage at any of the input leads is driven negative. It is due to the collector-base junction of the input PNP transistors becoming forward biased and thereby acting as input diode clamps. In addition to this diode action, there is also lateral NPN parasitic transistor action

## **Electrical Characteristics** (Continued)

on the IC chip. This transistor action can cause the output voltages of the comparators to go to the  $V^+$  voltage level (or to ground for a large overdrive) for the time duration that an input is driven negative. This is not destructive and normal output states will re-establish when the input voltage, which was negative, again returns to a value greater than -0.3V.

Note 4: These specifications are limited to  $-55^{\circ}\text{C} \le T_A \le +125^{\circ}\text{C}$ , for the LM193/LM193A. With the LM293 all temperature specifications are limited to  $-25^{\circ}\text{C} \le T_A \le +85^{\circ}\text{C}$  and the LM393 temperature specifications are limited to  $0^{\circ}\text{C} \le T_A \le +70^{\circ}\text{C}$ . The LM2903 is limited to  $-40^{\circ}\text{C} \le T_A \le +85^{\circ}\text{C}$ .

Note 5: The direction of the input current is out of the IC due to the PNP input stage. This current is essentially constant, independent of the state of the output so no loading change exists on the reference or input lines.

Note 6: The input common-mode voltage or either input signal voltage should not be allowed to go negative by more than 0.3V. The upper end of the common-mode voltage range is  $V^+$ -1.5V at 25°C, but either or both inputs can go to 36V without damage, independent of the magnitude of  $V^+$ .

Note 7: The response time specified is for a 100 mV input step with 5 mV overdrive. For larger overdrive signals 300 ns can be obtained, see typical performance characteristics section.

**Note 8:** Positive excursions of input voltage may exceed the power supply level. As long as the other voltage remains within the common-mode range, the comparator will provide a proper output state. The low input voltage state must not be less than -0.3V (or 0.3V below the magnitude of the negative power supply, if used).

Note 9: At output switch point,  $V_0 \approx 1.4V$ ,  $R_S = 0\Omega$  with  $V^+$  from 5V to 30V; and over the full input common-mode range (0V to  $V^+ = 1.5V$ ), at 25°C.

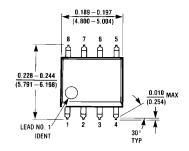
Note 10: Refer to RETS193AX for LM193AH military specifications and to RETS193X for LM193H military specifications.

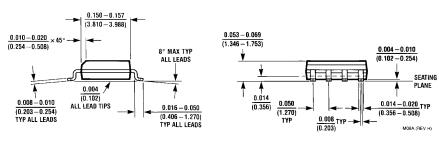
## **Ordering Information**

Package	Temperature Range	Part Number	NSC Drawing						
		LM193H*							
		LM193H/883							
		LM193H-MLS							
	-55°C to 125°C	LM193AH-MLS							
8-Pin Metal Can		LM193AH-QMLV**	H08C						
		LM193AH							
		LM193AH/883							
	−25°C to 85°C	LM293H							
	0°C to 70°C								
		LM193J/883*							
8-Pin Ceramic DIP	-55°C to 125°C	LM193AJ/883	100 A						
8-Pin Ceramic DIP	-55 C to 125 C	LM193AJ-QMLV**	J08A						
		LM193AJ-MLS							
8-Pin Molded DIP	0°C to 70°C	LM393N	N08E						
6-PIII Wolded DIP	−40°C to 85°C	LM2903N	NUOE						
	0°C to 70°C	LM393M							
8-Pin SOIC	0 0 10 70 0	LM393MX	M08A						
8-PIN 50IC	-40°C to 85°C	LM2903M	WUSA						
	-40 C to 85 C	LM2903MX							
	0°C to 70°C	LM393TL							
8-Bump (12 mils)	0°C to 70°C	LM393TLX	TI A00 A A A						
micro SMD	-40°C to 85°C	LM2903ITL	- TLA08AAA						
	-40 C 10 00 C	-40 C to 85 C LM2903ITLX							

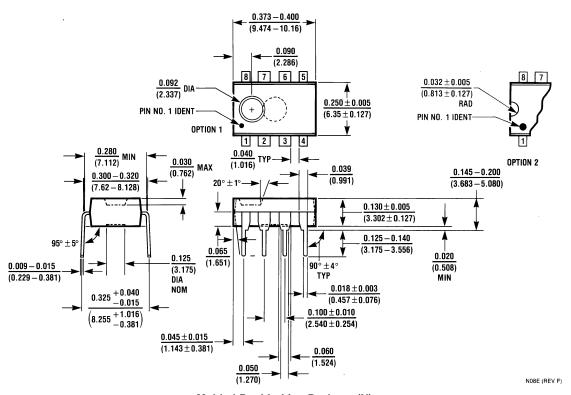
**Note:** \* Also available per LM38510/11202 **Note:** \*\* See STD Mil DWG 5962-94526

## Physical Dimensions inches (millimeters) unless otherwise noted (Continued)





SOIC Package NS Package Number M08A



Molded Dual-In-Line Package (N) NS Package N08E